Parallel and Distributed Algorithms and Programs TD n°2 - P-RAM

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All documents are available on my website: http://hadriencroubois.com/#Teaching

Tree Root Finding

Here we give another example of a problem for the separation of EREW and CREW models. Let \mathcal{F} be a forest of binary trees. Each node *i* of a tree is associated to a processor P(i) and has a pointer toward its father father(i). We are looking for EREW and CREW algorithms so that each node finds the root of his tree (denoted by root(i)), and thus prove the advantage of concurrent reads.

Question 1

Part 1 -

a) Give a P-RAM CREW algorithm so that each node finds root(i). Show that your algorithm uses concurrent reads and gives its complexity.

Part 2 -

Givens Rotations on a Ring of Processors

In order to triangularise a matrix A of order n, one can use Givens rotations. The basic operation ROT(i, j, k) consists in combining the two lines i et j, where each of them must start with k - 1 zéros, to cancel the element at position (j, k):

$$\begin{pmatrix} 0 \ \dots \ 0 \ \mathbf{a}'_{i,\mathbf{k}} \ a'_{i,k+1} \ \dots \ a'_{i,n-1} \\ 0 \ \dots \ 0 \ \mathbf{0} \ a'_{j,k+1} \ \dots \ a'_{j,n-1} \end{pmatrix} \leftarrow \begin{pmatrix} \cos\theta \ -\sin\theta \\ \sin\theta \ \cos\theta \end{pmatrix} \begin{pmatrix} 0 \ \dots \ 0 \ \mathbf{a}_{i,\mathbf{k}} \ a_{i,k+1} \ \dots \ a_{i,n-1} \\ 0 \ \dots \ 0 \ \mathbf{a}_{j,\mathbf{k}} \ a_{j,k+1} \ \dots \ a_{j,n-1} \end{pmatrix}$$

The sequential algorithm can be written as follows:

We assume that a rotation ROT(i, j, k) can be executed in constant time, independently of k.

Question 2

- a) Adapt this algorithm to a linear network of n processors $\rightarrow P_1 \rightarrow P_2 \ldots \rightarrow P_n$.
- b) Same question with a bidirectional linear network of processors with only $\lfloor \frac{n}{2} \rfloor$ processors $\rightleftharpoons P_1 \rightleftharpoons P_2 \ldots \rightleftharpoons P_{n/2}$.

Part 3

Acceleration Factor

Question 3

- a) Consider a problem to solve, which includes a percentage f of inherently sequential operations. Show that the acceleration factor is limited by 1/f, regardless of the number of processors used. What lesson can we learn for the parallelization of a fixed size problem?
- b) We assume that to solve a problem of size $n \times n$:
 - the number of arithmetic operations to execute n^{α} , with α a constant;
 - the number of elements to store in memory is w_1n^2 , with w_1 constant;
 - the number of input/output operations (intrinsically sequentials) is w_2n^2 , with w_2 a constant.

How can we estimate the acceleration obtained with p processors on a problem of large size? We can use a new definition of the acceleration factor: $S_p = \frac{A(1)}{A(p)}$, where A(p) is the mean time of an arithmetic operation with p processors and for a problem of fixed size? What lesson can we learn for the parallelization of a problem with variable size?

c) Give some examples of sublinear acceleration factors (i.e. with an efficiency strictly greater than 1).